φ ń -2b /2a 4 T rack 00 ×× × × × . △ △ 쥕 5 #.p ठ 8 101 <u>စ</u> 3

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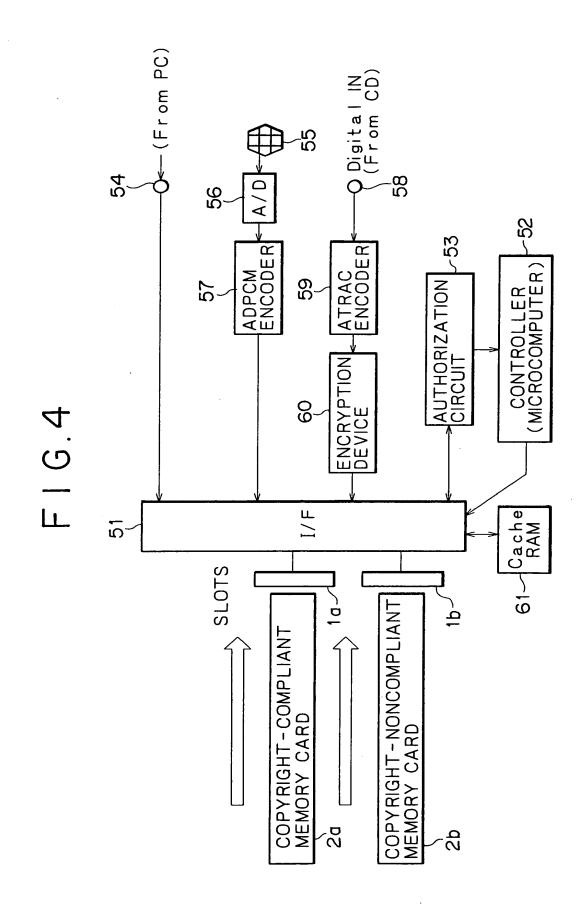
F 1 G.1

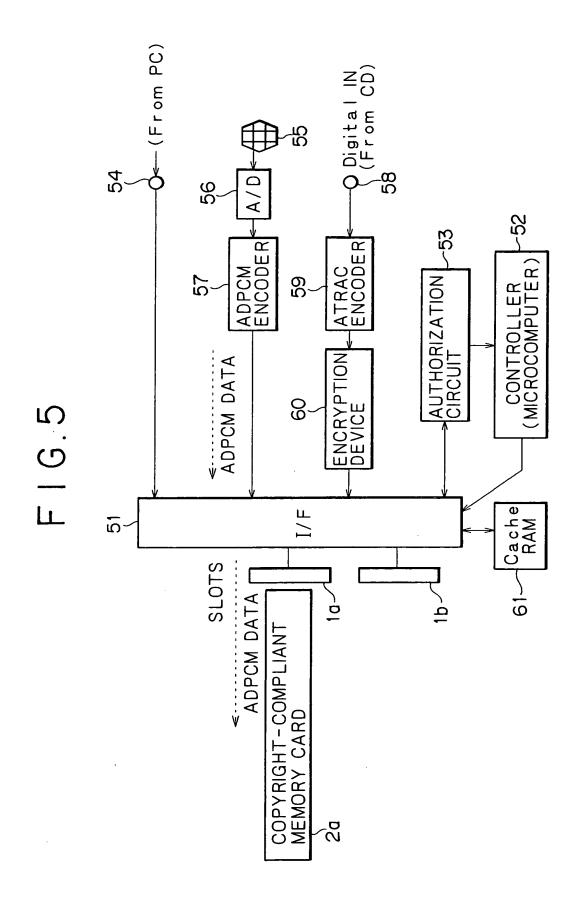
\<u>2a</u> FLASH MEMORY SEQUENCER OSC Cont. FLASH MEMORY I/F 3 4 RESET -35 30 COMMAND Reg. WR.Reg. RD.Reg. PAGE BUFFER Config ROM **£** ECC ECC DES ∑ > Z | |S/P&P/S|27 368 24 34 (23 I/F 2 32 33~ Reserv Reserv SBS SCK GND DIO GND INI

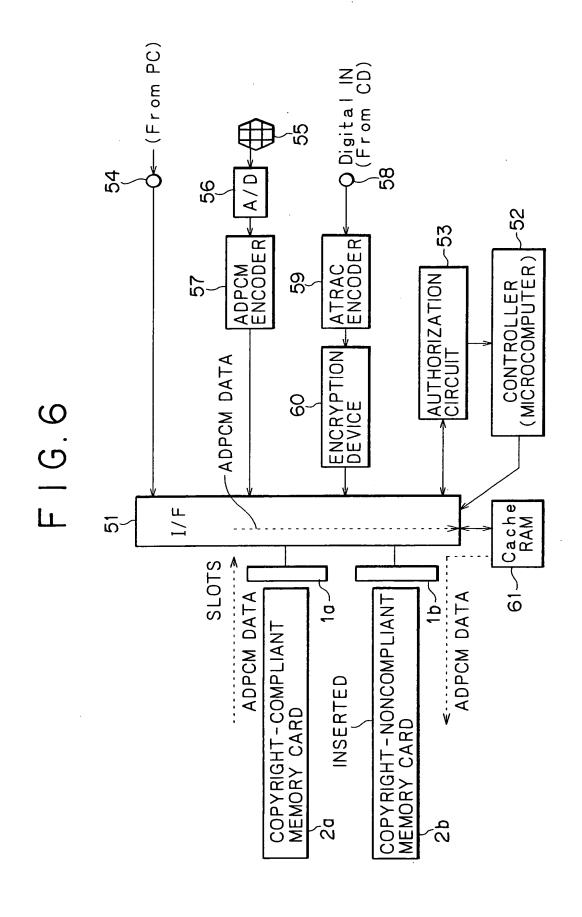
F 1 G. 2

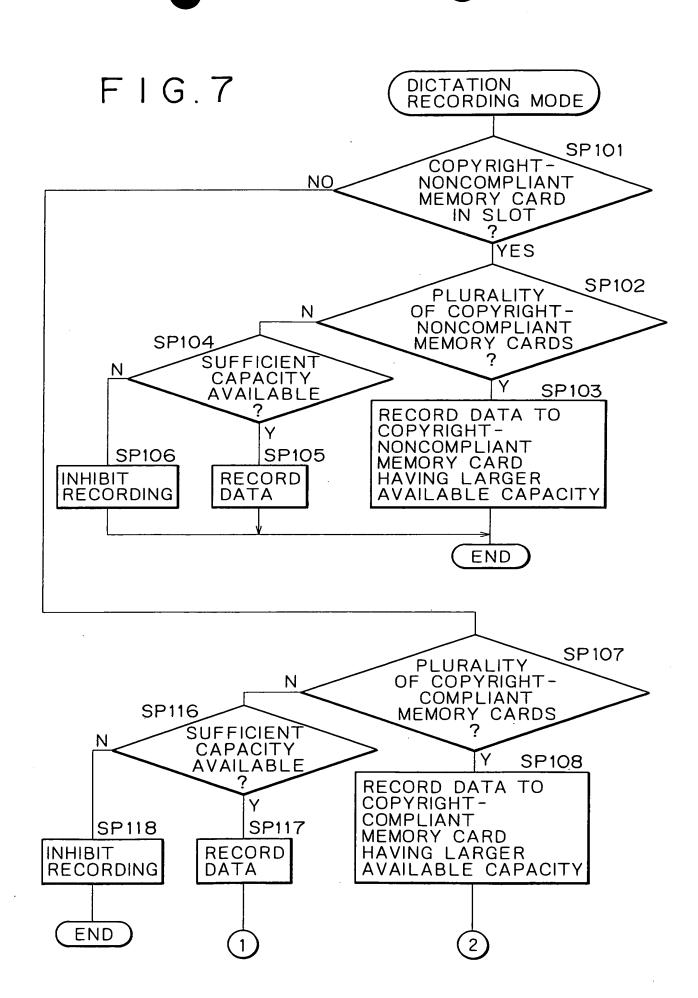
21 FLASH MEMORY SEQUENCER OSC Cont. FLASH MEMORY 1/F 4 RESET 8 COMMAND Reg. RD.Reg. PAGE BUFFER WR.Reg. Config ROM ECC ECC | S/P&P/S|27 28 29 25/ (23)I/F2/ Reserv Reserv SBS 010 SCK VCC GND GND INI

F I G. 3









F I G.8

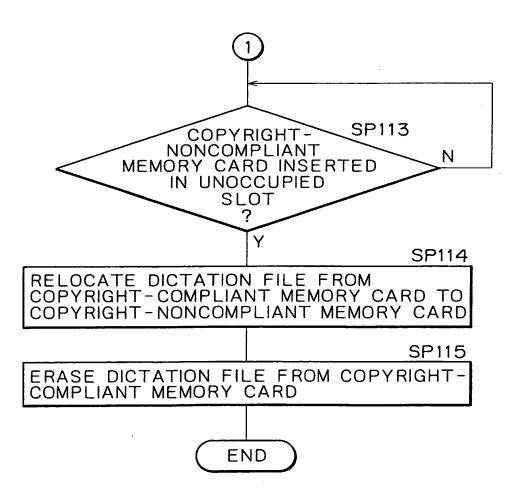
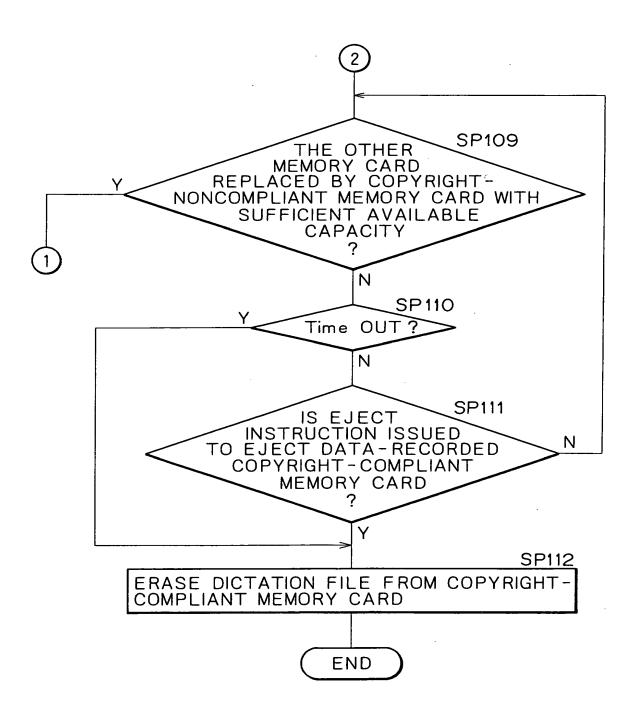


FIG.9



F I G.10

(From PC) O Digital IN (From CD) 55 54 56 -52 ATRAC AND ENCRYPTED DATA -53 ADPCM ENCODER ATRAC ENCODER CONTROLLER (MICROCOMPUTER) 20 57 AUTHORIZATION CIRCUIT ENCRYPTION DEVICE 0 Cache RAM I/F 5 SLOTS 61/ ATRAC AND ENCRYPTED DATA 1a/ COPYRIGHT-NONCOMPLIANT MEMORY CARD 1b-

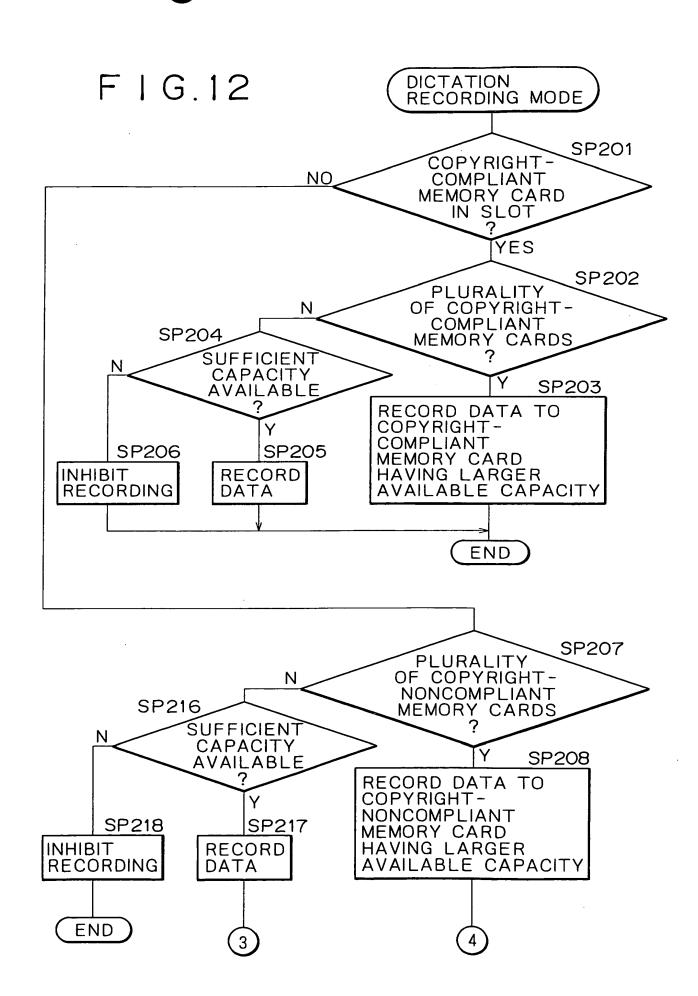
(From PC) O Digital IN (From CD) 55 54 28 A/D 56 -53 ADPCM ATRAC ENCODER -ATRAC AND ENCRYPTED DATA 29 AUTHORIZATION CIRCUIT 57 ENCRYPTION DEVICE 09 F I G.11 Cache I/F 5 SLOTS ATRAC AND ENCRYPTED DATA ATRAC AND ENCRYPTED DATA COPYRIGHT-NONCOMPLIANT 10 COPYRIGHT-COMPLIANT MEMORY CARD INSERTED MEMORY CARD \2b

-52

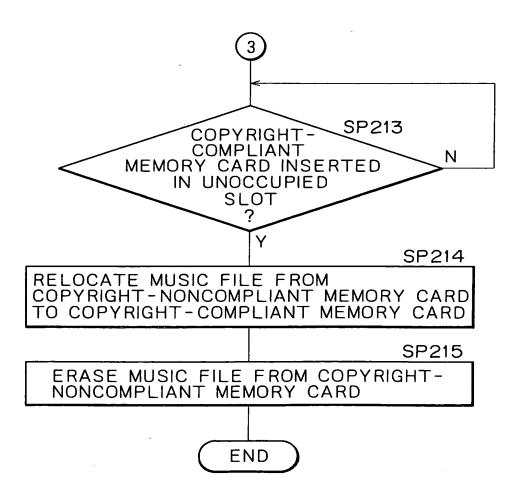
CONTROLLER (MICROCOMPUTER)

61/

DSETAL.OSCO



F I G.13



F I G.14

